09/752,750

REMARKS

Reconsideration of the above referenced application in view of the enclosed amendment and remarks is requested. Applicant notes with appreciation, the Examiner has removed the restriction requirement and examined all of the pending Claims 1-37 and 40-45. Applicant further notes with appreciation, the Examiner has indicated that Claims 43 and 24-30 are allowed, and that Claims 7, 9-12, 14-17, 23, 35, 40-42 and 44-45 would be allowable if rewritten to overcome the rejections under 35 U.S.C. § 112, second paragraph. Claims 1-6, 8, 13, 18-22, 31-34, and 36-39 are canceled. Claims 7, 9-12, 14-17, 23, 35, 40, and 42 are currently amended. Claims 46-48 are added to include corresponding method and system claims similar to the Claims that have been indicated as being allowable. Claims 7, 9-12, 14-17, 23-30, 35, and 40-48 are pending in the application.

ARGUMENT

Claims 5-7, 9-12, 14-17, 40-42, and 44-45 are rejected under 35 U.S.C. § 112, second paragraph as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards has the invention. This rejection is respectfully traversed and Claims 5-7, 9-12, 14-17, 40-42, and 44-45 are believed allowable as amended based on the following discussion.

The Examiner asserts that various "operators" (limitations "AND" operator and "OR" operator, for instance) recited in the claims are indefinite because they do not clearly define the structure nor require a particular structure to perform the operation. For examination purposes, the Examiner has considered these operators as just operations to extract a sign bit and generate an adjustment value, respectively. The rejected claims have been amended to recite the invention without using the offending structure, or asserted lack thereof. For instance, Claim 7 now recites a portion generator to compute a fractional portion of the input value by subtracting using the second integer from the input value, generating a fractional portion of the input value, where the portion generator is shown at least in Figure 4, 406. The other claims are similarly amended to overcome the Examiner's rejection.

09/752,750

The Examiner has indicated that Claims 43 and 24-30 are allowed, and that Claims 7, 9-12, 14-17, 23, 35, 40-42 and 44-45 would be allowable if rewritten to overcome the rejections under 35 U.S.C. § 112. Regarding Claim 23, the Examiner has made no specific rejection of this Claim. Claim 23 has been amended to put it into independent form, including the limitations of its parent Claim 21. Thus Claim 23 is in condition for immediate allowance along with Claims 43 and 24-30. The amendments to Claims 7, 9-12, 14-17, 35, 40-42 and 44-45 are believed to put them into condition for immediate allowance. Thus, all of the remaining claims are allowable.

CONCLUSION

In view of the foregoing, Claims 7, 9-12, 14-17, 23-30, 35, and 40-48 are all in condition for allowance. If the Examiner has any questions, the Examiner is invited to contact the undersigned at (703) 633-6845. Early issuance of Notice of Allowance is respectfully requested. Please charge any shortage of fees in connection with the filing of this paper, including extension of time fees, to Deposit Account 50-0221 and please credit any excess fees to such account.

Respectfully submitted,

Dated: 6/24/2004

Joni D. Stutman-Horn

Patent Attorney

Intel Corporation

Registration No. 42,173

(703) 633-6845

c/o Intel Americas LF3 4030 Lafayette Center Drive Chantilly, VA 20151